



# Vivekanand Education Society's Institute of Technology

(Autonomous Institute Affiliated to University of Mumbai, Approved by AICTE & Recognised by Govt. of Maharashtra)

NAAC accredited with 'A' grade

**Dr. (Mrs.) J. M. Nair**

M. Tech., Ph.D. (IIT-B)  
Principal

Ref. No.: VESIT/

Date: 28/05/2024

## Notice for the Revised Time Table of Re-exam Summer 2024

The following branch papers are rescheduled due to the Mumbai University Examination.

Revised dates of the papers are as shown below:

Semester	Branch	Subject	Earlier date of Papers	Revised date of Papers
VI	Electronics	Basic VLSI Design	06/06/2024	13/06/2024
IV	Electronics & Computer Science	Electronic Circuits	07/06/2024	13/06/2024
	Automation & Robotics	Embedded System		
IV	Automation & Robotics	Signal Conditioning Circuit Design	11/06/2024	14/06/2024
	Electronics and Telecommunication	Signals & System		
	Information Technology	Automata Theory		
VI	Information Technology	Department Optional Course :-2 1. Software Architecture 2. Image Processing 3. Green IT 4. Ethical Hacking and Forensic	12/06/2024	14/06/2024

The time & venue for all the above examination will remain the same.

Please referred college website for Revised Re-exam Time Table Summer 2024.

**Principal**

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VIVEKANAND EDUCATION SOCIETY'S  
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